





MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

Sponsored by CMR Educational Society

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DEPARTMENT OF INFORMATION TECHNOLOGY II B.TECH I SEMESTER R17 SUPPLEMENTARY PREVIOUS QUESTION PAPERS



LIST OF SUBJECTS

CODE	NAME OF THE SUBJECT
R17A0510	Computer Organization
R17A0504	Data Structures using C++
R17A0503	Mathematical Foundation of Computer Science
R17A0024	Probability and Statistics
R17A0401	Electronic Devices and Circuits
R17A0461	Digital Logic Design

Code No: R17A0510 MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, Dec-21/Jan-22

Computer Organization

$(CSE \otimes II)$										
Roll No										

Time: 3 hours

Max. Marks: 70

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

SECTION-I

1 Describe the functional blocks of a computer. Explain the RTL interpretation of [14M] instructions.

OR

2 Illustrate the various addressing modes of the CPU. Brief on fixed and floating point [14M] representation of relevant data.

SECTION-II

3	Describe the phases in Instruction cycle.	[14M]
	OR	
4	Illustrate with a neat architecture about design of control unit	[14M]
	SECTION-III	
5	Explain in detail about CISC and RISC machines	[14M]
	OR	
6	Demonstrate with example the working of shift & add and booths multiplier.	[14M]
	SECTION-IV	
7	Briefly describe the modes of data transfer in detail.	[14M]
	OR	
8	List various parallel processing challenges. Draw the block diagram of 5 stage	[14M]
	pipeline system.	
	SECTION-V	
9	Explain briefly about Associate-mapped and set-associate mapped cache memory	[14M]
	OR	
10	Describe about the segmented page mapping and page replacement in detail	[14M]

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Questio	on from ea	ch SECTION and	l each	Questic	on carrie	es 14 i	marks	5.				
				* SECT	** TANT							
1	a) Expla	in the process h	ow to	analyze	<u>ION-I</u> the ti	me an	id sna	nce co	omnl	exiti	ies for a	[7M]
-	recurs	sive function with	n an ex	ample.		ine un	la spa		, mpi	Unit.	105 101 u	[,]
	b) Write	a C++ program to	o searc	h for th	e given	key el	lemen	it in ai	rray	usin	g Binary	
	Searc	h.			D							[7M]
2	Write a ($\gamma \perp program to g$	ort an	Orray y	VK vith n a	laman	te in	ascon	dina	r ord	lor using	[1/IM]
4	Ouick So	ort. Explain the pr	ocess v	with sui	table ex	xampl	e.	ascen	umg	5 010	ici using	[1411]
		1 1		SECT	ION-II	. 1						
3	Implemen	nt List ADT with	insert	and del	ete ope	ration	s at v	arious	s pos	sitio	ns.	[14M]
4	a) Immla		Г :	С	R							[7]\[]
4	b) Const	truct the Binary T	ree us	g arrays ing the	followi	no tre	e trav	ersals				[/] VI]
	Inord	er Traversal : { 4	, 2, 1, [′]	7, 5, 8, 1	3, 6 }	115 110	e nuv	ersuit				
	Preor	der Traversal: { 1	, 2, 4,	3, 5, 7,	8, 6 }							[7M]
-	F 1 · 4	1 (D 1	1	<u>SECT</u>	[<u>ON-II</u>]	<u>[</u>		1				[1 4) (1
5	Explain t	he process of Pol	yphase	e merge	With SU	iitable	e exan	nple.				[14]1]
6	What is a	priority Oueue?	Implei	nent its	operati	ions.						[14M]
		1 7 5	1	SECT	<u>ION-IV</u>	7						
7	What is	a Dictionary Da	ta Stru	cture?	Explain	n its r	repres	entati	ions	witl	h proper	[14M]
	examples			0	D							
8	What is c	collision in Hashi	ng? Ar	only line	ear prof	oing ai	nd au	adrati	c pr	obin	g for the	[14M]
-	following	g elements with th	ne table	e size as	s 15.	8	1		- r -		8	[]
	12	2, 78, 98, 23, 45,	32, 60	, 5, 89,	56, 31,	46						
0	W/h c t = :	4h a 1imaid - 4i - 1	f a D!	<u>SECT</u>	ION-V	-9 117	h . 4	A X 7T	T.	-9.0		Г1 <i>А</i> Л <i>А</i> Л
У	AVI tree	for the following	i a Bin v elem	ary Sea	ircn Tre	e? W	nat 1s	AVL	, i re	e? C	onstruct	[14][1]
		H, I, J, B, A, E.	C, F, I	D, G, K	, L							
			. ,	С	R							
10	a) What	are the Graph tra	versal	s? Expl	ain witl	h an e	xamp!	le.				[7M]
	b) What	is a B-tree? Expl	ain ins	sert and *****	delete *****	operat	tions v	with a	ın ex	amp	Die.	[7] NI]

Code No: **R17A0503** MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India) II B.Tech I Semester Supplementary Examinations, Dec-21/Jan-22 Mathematical Foundation of Computer Science

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Roll No							

Time: 3 hours

Note: This question paper Consists of 5 Sections. Answer FIVE Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks. ***

SECTION-I

1	a.	Simplify the following compound proposition using the laws of logic.	[7M]
		$(p \vee q) \wedge [\sim((\sim p) \vee q)]$	
	b.	Obtain PCNF of the following. $p \rightarrow \{(p \rightarrow q) \land \neg(\neg q \lor \neg p)\}$	[7][1]
2	0	Obtain the principal conjugative and disjunctive normal forms of $(-\mathbf{P}, \mathbf{P}) \land (\mathbf{O}, \mathbf{P})$	[7]\/[]
4	a. h	Find the principal disjunctive normal form of $P \rightarrow \{(P \rightarrow Q) \land \neg (\neg Q \lor P)\}$	[7]N1]
	0.	SECTION-II	
3	a.	Let $A = \{1, 2, 3, 4, 6, 8, 12, 24\}$, show that the relation 'divides' is partial ordering on A and draw Hasse diagram.	[7M]
	b.	Determine whether the relation is reflexive, symmetric, anti-symmetric, and transitive. Let A= $\{1,2,3,4\}$ and R= $\{(1,1),(1,2),(1,3),(2,3),(3,1),(2,4),(4,4)\}$ and find whether R is equivalent? If yes find the partition of A induced by R. OR	[7M]
4	a.	Draw the Hasse diagram representing the partial ordering $\{(a, b) a \text{ divides } b\}$ on $\{2, 3, 6, 12, 24, 36\}$.	[7M]
	b.	Explain partial ordering relation with example.	[7M]
_		SECTION-III	
5	a.	How the fuzzy logic can be applicable to the application of room temperature control?	[7M]
	b.	If o is an operation on Z defined by x o $y = x + y + 1$, prove that (z, o) is an abelian group?	[7M]
		OR	
6	a.	Prove that a group G in which every element is its own inverse is abelian.	[7 M]
	b.	If (G, *) is an abelian group then prove that $(a * b)^n = a^n * b^n$ for all n belongs to N	[7M]
		SECTION-IV	
7	a.	State and prove binomial theorem.	[7M]
	b.	Solve the inhomogeneous recurrence relation $a_{n+2} - 6a_{n+1} + 9a_n = 7(3)^n$ where $a_0 = 1$ and $a_1 = 4$	[7M]
		OR	
8	a.	Define Pigeonhole Principle and its Applications.	[7M]
	b.	A sequence is defined by the recurrence relation $a_{n+1} = -3a_n + 7$ with $a_0 = 2$ What is the value of a_2 ?	[7M]

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Max. Marks: 70

SECTION-V

9	a.	Explain planner graphs with example.	[7M]
	b.	What is spanning tree?	[7M]
		OR	
10	a.	Sketch the following given graphs neatly. K_5 , K_6 , $K_{3,4}$, C_6 , and W_6 .	[7M]

a. Sketch the following given graphs neatly. K₅, K₆, K_{3,4}, C₆, and W₆. [7M]
b. Define the chromatic number and find the chromatic number of the below graph. [7M]



Code No: R17A0024 MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous Institution – UGC, Govt. of India) II B.Tech I Semester Supplementary Examinations, Dec-21/Jan-22 Probability and Statistics



Roll No

Time: 3 hours

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

*** SECTION-I

1 A Continuous random variable X has the distribution function

 $F(x) = \begin{cases} 0, \text{ if } x \le 1 \\ K(1-x)^{4}, \text{ if } 1 < x \le 3 \\ 1, \text{ if } x > 3 \end{cases}$ Determine i. f(x) ii. K iii. Mean

OR

If the weights of 300 students are normally distributed with mean 68kgs and [14M] Standard deviation 3 kgs. How many students have weight?
i. Greater than 72 kgs

ii. Less than or equal to 64 kgs

iii. Between 65 and 71 kgs inclusive

SECTION-II

3 Calculate the coefficient of Rank Correlation

X	68	64	75	50	64	80	75	40	55	64
у	62	58	68	45	81	60	68	48	50	70

OR

4 The equations of two regression lines are 7x-16y+9=0 and 5y-4x-3=0. [14M] Find the Coefficient of Correlation and the means of x and y.

SECTION-III

- 5 Population consists of five numbers 5,10,14,18,13 and 24 .Consider all [14M] possible samples of size two which can be drawn without replacement from the population. Find
 - a. The Mean of the population
 - b. The Variance of the population
 - c. The Standard deviation of the population
 - d. The mean of the Sampling distribution of Means
 - e. The Standard deviation of Sampling distribution of means.

Max. Marks: 70

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[14M]

[14M]

A sample of 900 members has a mean of 3.4cms and S.D 2.61cms. If this sample [14M] has been taken from a large population of mean 3.25cm and S.D of 2.61cms. Test at 5 % level of significance and also construct 95% confidence limits of true mean.

SECTION-IV

7 The life time of electric bulbs for a random sample of 10 from a large [14M] consignment gave the following data

0	0			\mathcal{O}						
Item	1	2	3	4	5	6	7	8	9	10
Life in	1.2	4.6	3.9	4.1	5.2	3.8	3.9	4.3	4.4	5.6
1000										
hrs										

Can we accept the hypothesis that the average life time of bulbs is 4000hrs.

OR

8 Pumpkins were grown under two experimental conditions. Two random samples [14M] of 11 and 9 pumpkins, show the sample standard deviations of their weights as 0.8 and 0.5 respectively. Assuming that the weight distributions are normal, test the hypothesis that the true variances are equal.

SECTION-V

9 Consider a box office ticket window being manned by a single server. [14M] Customers arrive to purchase tickets according to Poisson input process with a mean late of 30 per hour .The time required to serve a customer has an exponential distribution with a mean of 910 seconds. Determine the following.
a. Fraction of the time the server busy b. The average number of customers queuing for service.

OR

10Describe the classification of the states of Markov process. What is[14M]homogenous Markov Chain .

Code No: R17A0401 MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, Dec-21/Jan-22 Electronic Devices and Circuits

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Roll No										

Time: 3 hours

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

*** SECTION-I

1a. Describe the working principle of an SCR with V-I Characteristics.[10M]b. Determine the forward resistance of a Silicon PN junction diode when the
forward current is 6 m A at room temperature[4M]

OR

a. Describe with the help of a relevant diagram, The construction of photo diode [10M] and explain its working?
b. Determine the forward bias voltage applied to a silicon diode to cause a [4M]

forward current of 10mA and reverse saturation current $I_0=25 \times 10^{-7}$ A at room temperature

SECTION-II

3 Explain the working of a half wave rectifier and derive expression for **[14M]** Rectification Efficiency, Ripple Factor and Transformer Utilization Factor of a half wave rectifier with resistive load

OR

4 Explain the operation of Full wave rectifier with center tap transformer and also [14M] derive ac and dc voltage and current, ripple factor and efficiency

SECTION-III

5 Derive the expression for current gain, voltage gain, input and output impedances [14M] of a CC amplifier using h- parameter exact and approximate analysis.

OR

6 Explain the input and output characteristics of CE configuration and from the [14M] output characteristics explain different regions of operation of transistor.

SECTION-IV

7 a. Draw the circuit of voltage divider biasing and derive the expression for stability factor.
b. A silicon transistor having β=52 and V_{BE}=0.7Vis used in voltage divider [6M] biasing circuit. V_{CC}=25V and R_L=5KΩ. The operating point is required to be established at V_{CE}=10V and I_C=2mA and stability factor S not exceeding 4. Draw the circuit and find the value of R₁, R₂ and R_E.

OR

8 a. Define stability factor and derive an expression for stability factor of fixed [7M] bias [7M]

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Max. Marks: 70

b. A transistor uses potential divider biasing with $R_1{=}50K\Omega,\,R_2{=}10K\Omega$ and $R_E{=}1K\Omega.$ If $V_{CC}{=}12V,\,find$

i) I_C; given V_{BE}=0.1V

ii) I_C; given $V_{BE}=0.4V$

SECTION-V

[6]
[8M]
[6M]



Code No: R17A0461 MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous Institution – UGC, Govt. of India) II B.Tech I Semester Supplementary Examinations, Dec-21/Jan-22 Digital Logic Design

(IT)										
Roll No										

Time: 3 hoursMax. Marks: 70Note: This question paper Consists of 5 Sections. Answer FIVE Questions, Choosing ONEQuestion from each SECTION and each Question carries 14 marks.

1	a) Show that AB'C+B+BD'+ABD+ABD'+A'C = B+C b) Papeling logical XOP acts using NAND and NOP Cates	Marks [7M]
	b) Realize logical XOR gate using NAND and NOR Gates	
2	a) Convert the following to Decimal and then to octal	[9M]
	(i) $(125F)_{16}$ (ii) $(10111111)_2$ (iii) $(392)_{16}$ b) Apply 2's Complement Binary Subtraction for the following numbers $25_{(10)}$ - $36_{(10)}$	[5M]
3	a) Implement the following Boolean function with NAND gates only	[7 M]
5	$F(X Y Z) = \Sigma m (1 2 3 4 5 7)$	[/ []]
	b) Explain Prime Implicants and Essential Prime Implicants with an example OR	[7M]
4	 a) Explain the advantage of Quine-McCluskey method with K-Map b)Minimize the following logic function using k-map and realize using NOR gates. 	[4M] [10M]
	$F(A,B,C,D) = \Sigma m(1,3,5,8,9,11,15) + \Sigma d(2,13)$	
	SECTION-III	
5	a)Design a BCD to Excess-3 code converter and realize with minimum no. of gates	[7M]
	b)Implement the 8X1 Multiplexer using the function	[7M]
	$F(A,B,C,D) = \Sigma (1,3,4,11,12,13,14,15).$	
	OR	
6	 a)Design a combinational circuit to realize full-adder using NAND-gates only b) Brief about Priority encoder with two inputs. SECTION-IV 	[7M] [7M]
7	List out the following for all flip flops (SR, D, JK) a. Logic symbol	[14M]
	b. Characteristic table	
	c. Logic diagram	
	d. Excitation table	
0	OR	F#2 23
8	(a) Realize SR flip-flop using T flip-flop.	[7M]

	(b) Compare level triggering and edge triggering with neat timing diagram. SECTION-V	[7M]
9	a) What are the advantages and disadvantages of using a PROM as a PLD b) What is ROM? List the different types of ROMs.	[7M] [7M]
	OR	
10	a) Explain about RAM in detail.	[5M]
	b) Implement the following Boolean functions with a PLA.	[9M]
	$F1(A,B,C)=\Sigma m(0,1,2,4)$	
	$F2(A,B,C)=\Sigma m(0,5,6,7)$	
	$F3(A,B,C)=\Sigma m(0,3,5,7)$	
